









LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION			REFERENCE DESIGNATOR
					BC	BF	BH	
1	1	B-DD-M8743-BA		DWG DIRECTORY	REF	REF	REF	
2	2	D-UA-M8743-BA-0		UNIT ASSY	REF	REF	REF	
3	3	D-MD-5015117-0-0		CIRCUIT DRILL & ETCH	REF	REF	REF	
4	4	D-EC-5015117-BA-0		ETCH CUT DWG	REF	REF	REF	
5	5	D-CS-M8743-BA-MS01		CIRCUIT SCHEMATIC	REF	REF	REF	
6	6		1105275-00	D 672 TR= 15NS PIV= 60V SI	1	1	1	D1
7	7		5015117-00	DRILL AND ETCH BRD.	1	1	1	
8	8		1005306-00	6.8MFD 35V 10% S.TANT	1	1	1	C1
9	9		1012084-03	150 MFD 15V +75-10% AL EL	2	2	2	C5,C6
10	10		1018200-00	220.0 MMF 50V 1% CER	2	2	2	C2,C3
11	11		1014265-02	.33 MFD 50V +80-20% CER	142	142	142	C4,C7-C147
12	12		1117373-00	LED ASSY GREEN	1	1	1	D3
13	13		1117373-01	LED ASSY RED	1	1	1	D2
14	14		1211164-00	SW,DIP 4POS/1PST 5VDC100MA F	1	1	1	E28
15	15		1211164-04	SW,DIP 08POS/1PST 5VDC100MA F	1	1	1	E38
16	16		1216988-02	HANDLE,MODULE,HEX TWO EJECTORS	1	1	1	
17	17		1300229-00	100.0 .25 W 5.0 % CF	2	2	2	R5,R22
18	18		1300316-00	470.0 .25 W 5.0 % CF	2	2	2	R3,R4
19	19		1300447-00	4.70 K .25 W 5.0 % CF	3	3	3	R11,R12,R18
20	20		1301322-00	180.0 .25 W 5.0 % CF	1	1	1	R19
21	21		1301808-00	22.0 K .25 W 5.0 % CF	1	1	1	R1
22	22		1302124-00	18.0 .25 W 5.0 % CF	1	1	1	R2
23	23		1302128-00	17.80 .50 W 1.0 % RN60E-F 2	1	1	1	R16
24	24		1302858-00	100.0 .25 W 1.0 % RN55D-F10	1	1	1	R7
25	25		1303064-00	75.0 .25 W 1.0 % RN55D-F10	1	1	1	R6
26	26		1304858-00	348.0 .25 W 1.0 % RN55D-F10	1	1	1	R8
27	27		1305123-00	215.0 .25 W 1.0 % RN55D-F10	1	1	1	R9
28	28		1311522-00	200.0 .25 W 5.0 % CF	1	1	1	R13
29	29		1316395-00	R NETWORK 9-4.7K 2.0 % 10PIN	5	5	5	R14,R15,R23,R25,R26
30	30		1316395-02	R NETWORK 9-1.0K 2.0 % 10PIN	2	2	2	R17,R24

REVISION HISTORY		BASIC PART NO: M8743		DRN: J. RICHARD		DATE: 10-FEB-82		D I G I T A L	
ENG	ECO NUMBER	REV	SECTION A OF A	CHK'D: P. BOSSMAN		DATE: 17-FEB-82		TITLE PARTS LIST	
JJ	INITIAL	A	SECTION.VARIATION INDEX	[A] BC,BF,BH		MS11-P MEMORY			
				[B]					
				[C]		DES.ENG: J. JANETOS		DATE: 17-FEB-82	
				[D]					
				[E]					
				[F]		RESP.ENG.: J. JANETOS		DATE: 17-FEB-82	
				[H]				DOCUMENT NUMBER	
				[J]				SIZE CODE NUMBER REV	
				[K]		MFG.ENG.: R. HILL		DATE: 17-FEB-82 K PL M8743-BA-DBP A	
				[L]					
				[M]		ASSEMBLY NUMBER:		TOP DOCUMENT NUMBER:	
				[N]		D-UA-M8743-BA-0		B-DD-M8743-BA	
								FILE NAME: Z2712A.PLS	
								EDIT #: 21	

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LINE	ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION			REFERENCE DESIGNATOR
					BC	BF	BH	
31	31		1317494-00	22.10 K .25 W 1.0 % RN55D-F10	2	2	2	R20,R21
32	32		1505321-00	DEC4258 PNF 200MW SI 12 30 M	2	2	2	Q1,Q2
33	33		1610033-00	DELAY= 10-100NS,10TAPS	1	1	1	E10
34	34		1909705-00	DEC 8881 NAND GATE-QUAD 2IN 0	1	1	1	E27
35	35		1910534-00	74S04 INVERTER GATE-HEX 1I	1	1	1	E12
36	36		1910536-00	74S10 NAND GATE-TRIPLE 3IN	1	1	1	E3
37	37		1910540-00	74S22 NAND GATE-DUAL 4INPU	1	1	1	E3
38	38		1910542-00	74S64 A-O-I GATE 4-2-3-2	2	2	2	E9,E17
39	39		1910544-00	74S74 FF-D DUAL,EDGE TRIGG	1	1	1	E6
40	40		1910546-00	74S140 NAND GATE-DUAL 4INPU	1	1	1	E4
41	41		1910547-00	74S153 MUX 1 OF 4 (DUAL)	4	4	4	E49,E50,E55,E56
42	42		1910549-00	74S158 MUX 1 OF 2 (QUAD)	1	1	1	E45
43	43		1910550-00	74S174 FF-D HEX	1	1	1	E40
44	44		1911469-00	DEC 8640 RECEIVER,BUS,QUAD,U	2	2	2	E1,E54
45	45		1911579-00	8641 TRANSCEIVER,BUS,QUA	4	4	4	E7,E13,E18,E22
46	46		1911944-00	555CN TIMER,FUNCT.BLOCK	1	1	1	E47
47	47		1911983-00	74S133 NAND GATE-POSITIVE 1	1	1	1	E37
48	48		1912388-00	74S02 NOR GATE-QUAD 2IN,PO	2	2	2	E2,E23
49	49		1912799-00	LS00 NAND-GATE-QUAD 2IN,P	1	1	1	E35
50	50		1912803-00	LS04 INVERTER GATE,HEX	1	1	1	E11
51	51		1912820-00	LS51 A-O-I GATE 2-WIDE 2I	1	1	1	E32
52	52		1912824-00	LS74 FF-D DUAL,EDGE TRIGG	1	1	1	E30
53	53		1912828-00	LS85 COMPARATOR,4BIT MAGN	1	1	1	E31
54	54		1912834-00	LS112 FF-JK,DUAL,EDGE TRIG	1	1	1	E8
55	55		1913340-00	74S32 OR GATE-QUAD 2IN	1	1	1	E26
56	56		1913670-00	74S373 LATCH 8BIT TRASP TR	3	3	3	E14,E16,E19
57	57		1913777-00	LS240 DRIVER,LINE,OCTAL,T	1	1	1	E43
58	58		1913887-00	74S258 MUX 1 OF 2(QUAD)TRI	1	1	1	E21
59	59		1914451-00	74LS393 COUNTER,BINARY,4BIT	2	2	2	E51,E57
60	60		1915219-00	LS373 FF-D OCTAL-TRANSPARE	2	2	2	E44,E46
61	61		1918822-00	2960 ERROR DETECTOR/CORRE	1	1	1	E15
62	62		1919004-00	DC 750 REGISTER,ADDRESS	1	1	1	E24
63	63		1919015-00	DC 021 BUS TRANSCEIVER,20PI	3	3	3	E36,E42,E48
64	64		1919095-00	2966PC DYNAMIC MEMORY DRIV	2	2	2	E61,E64
65	65		2118467-01	8264-20 RAM 64K X1,200NS 1	176	-	-	E100-E107,E110-E117,E120-E127, CONT E130-E137,E140-E147,E150-E157, CONT E160-E167,E170-E177,E180-E187, CONT E190-E197,E200-E207,E210-E217, CONT E220-E227,E230-E237,E240-E247, CONT E250-E257,E260-E267,E270-E277, CONT E280-E287,E290-E297,E300-E307, CONT E310-E317
66	66		1919635-01	2965 DRIVER,DYNAMIC RAM	5	5	5	E59,E60,E62,E63,E65
67	67		9000024-01	EYELET,ROLLED 0.1210DX0.192	12	12	12	
68	68		9009185-00	JUMPER, WIRE, INSULATED, BLACK B	4	4	4	W1-W4
69	69		1302388-00	2.0 K .25 W 5.0 % CF	1	1	1	R10
70	70		1910544-01	74S74-60GG-D DUAL,EDGE TRIG	1	1	1	E41
71	71	2 EACH IC	1215006-06	SKT,IC 24PIN DIP TIN SOLD	4	4	4	XE24,XE15

DI	IG	IT	AL	TITLE	MS11-P MEMORY	SECTION A	OF A	SIZE	CODE	DOCUMENT NUMBER	REV
								K	PL	M8743-BA-DBP	A

LINE ITEM	DOCUMENT NUMBER	PART NUMBER	DESCRIPTION	QTY PER VARIATION			REFERENCE DESIGNATOR
				BC	BF	BH	
72	72	1219911-01	SKT,IC 24PIN DIP	4	4	4	XE20,XE25,XE29,XE34
73	73	23061D2-00	D2-02	1	1	1	E20
74	74	23062D2-00	D2-02	1	1	1	E25
75	75	23063D2-00	D2-02	1	1	1	E29
76	76	23064D2-00	D2-02	1	1	1	E34
77	77	23086B1-00	B1-03	1	1	1	E33
78	78	23087B1-00	B1-03	1	1	1	E39
79	79	23344A1-00	A1-05	1	1	1	E58
80	80	2118470-01	4864-1 MOS RAM 64K X1,200	-	176	-	E100-E107,E110-E117,E120-E127, CONT E130-E137,E140-E147,E150-E157, CONT E160-E167,E170-E177,E180-E187, CONT E190-E197,E200-E207,E210-E217, CONT E220-E227,E230-E237,E240-E247, CONT E250-E257,E260-E267,E270-E277, CONT E280-E287,E290-E297,E300-E307, CONT E310-E317
81	81	2118472-01	4164-2 MOS RAM 64K X1,200	-	-	176	E100-E107,E110-E117,E120-E127, CONT E130-E137,E140-E147,E150-E157, CONT E160-E167,E170-E177,E180-E187, CONT E190-E197,E200-E207,E210-E217, CONT E220-E227,E230-E237,E240-E247, CONT E250-E257,E260-E267,E270-E277, CONT E280-E287,E290-E297,E300-E307, CONT E310-E317
82	82	D-CS-M8743-BA-MS02	CIRCUIT SCHEMATIC	REF	REF	REF	
83	83	D-CS-M8743-BA-MS03	CIRCUIT SCHEMATIC	REF	REF	REF	
84	84	D-CS-M8743-BA-MS04	CIRCUIT SCHEMATIC	REF	REF	REF	
85	85	D-CS-M8743-BA-MS05	CIRCUIT SCHEMATIC	REF	REF	REF	
86	86	D-CS-M8743-BA-MS06	CIRCUIT SCHEMATIC	REF	REF	REF	
87	87	D-CS-M8743-BA-MS07	CIRCUIT SCHEMATIC	REF	REF	REF	
88	88	D-CS-M8743-BA-MS08	CIRCUIT SCHEMATIC	REF	REF	REF	
89	89	D-CS-M8743-BA-MS09	CIRCUIT SCHEMATIC	REF	REF	REF	
90	90	D-CS-M8743-BA-MS10	CIRCUIT SCHEMATIC	REF	REF	REF	
91	91	D-CS-M8743-BA-MS11	CIRCUIT SCHEMATIC	REF	REF	REF	
92	92	D-CS-M8743-BA-MS12	CIRCUIT SCHEMATIC	REF	REF	REF	
93	93	D-CS-M8743-BA-MS13	CIRCUIT SCHEMATIC	REF	REF	REF	
94	94	9105740-55	WIRE(WRAP) 30AWG KYNAR UL14	A/R	A/R	A/R	

- 95 NOTE: M8743-BA IS THE PRIMARY VARIATION OF THE 512KX16 BITS SYSTEM (NOT A MODULE TYPE).
- 96 NOTE: M8743-BC IS A MODULE TYPE USING FUJITSU 64K MOS DEVICES.
- 97 NOTE: M8743-BF IS A MODULE TYPE USING HITACHI 64K MOS DEVICES.
- 98 NOTE: M8743-BH IS A MODULE TYPE USING NEC 64K MOS DEVICES.
- 99 NOTE: ON ITEM 71 USE 2 PER EACH IC
- 100 NOTE: THIS ASSEMBLY CONTAINS ITEMS SENSITIVE TO ELECTRO-STATIC DISCHARGE.

D I G I T A L										TITLE										MS11-P MEMORY										SECTION A OF A										SIZE CODE										DOCUMENT NUMBER										REV									
																																								K PL										M8743-BA-DBP										A									

AUTOMATED BY PRTLST.3P(41)

P A R T S L I S T

SHEET A4 OF A4

LINE ITEM DOCUMENT NUMBER PART NUMBER DESCRIPTION

QTY PER VARIATION

BC BF BH

REFERENCE DESIGNATOR

101 NOTE: MAINTAIN ANTI-STATIC PROTECTION PROCEDURES PER DEC STD 067.

D	I	G	I	T	A	L	TITLE	MS11-P MEMORY	SECTION A OF A	SIZE	CODE	DOCUMENT NUMBER	REV
										K	PL	M8743-BA-DBP	A



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8-VA-CHL8B UA 2 1



NOTES: 1. DO NOT INSTALL E52 & E53.

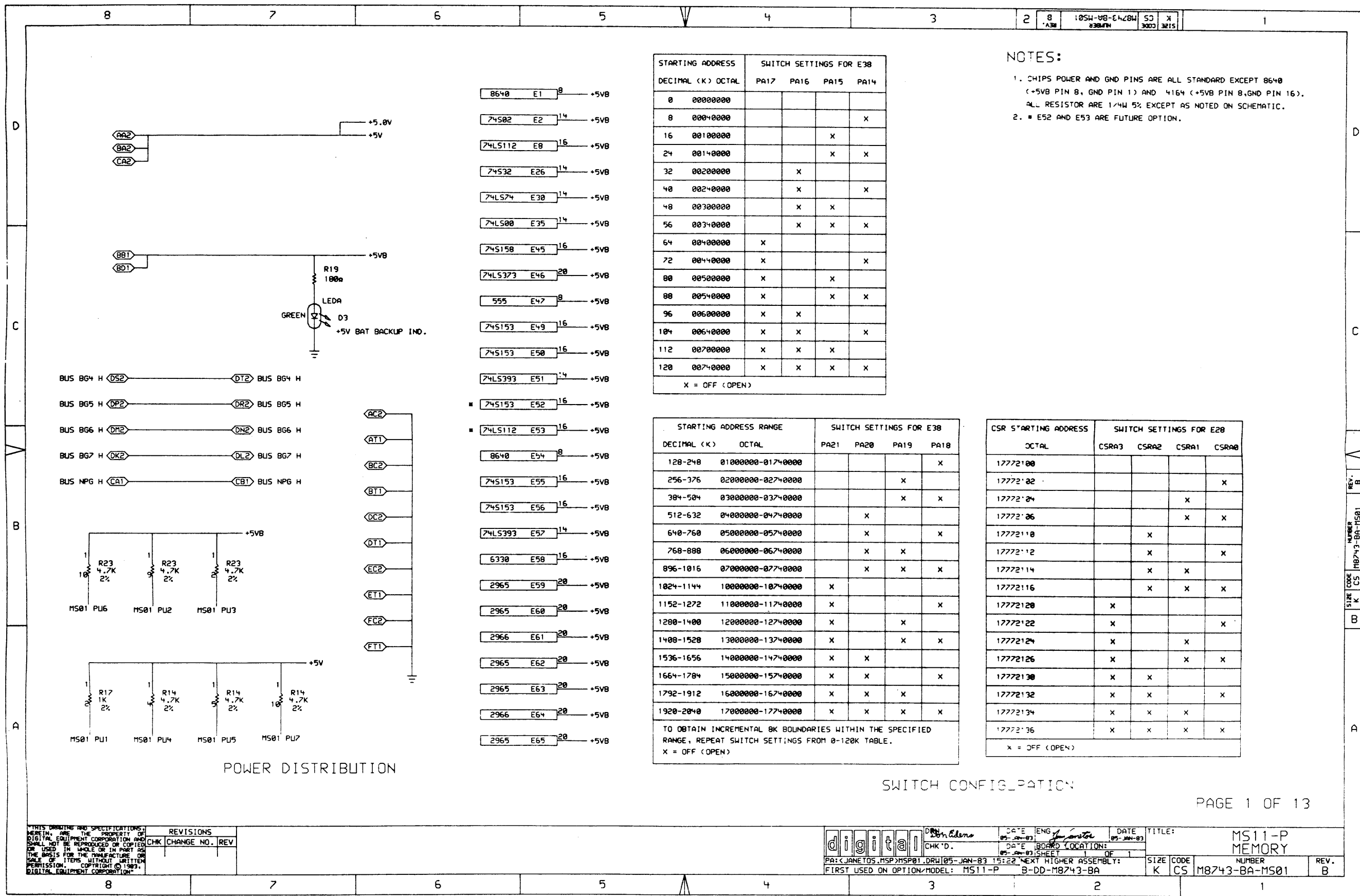
STEP E	+ Y AXIS	STEP TIMES
REPRT	+ X AXIS	STEP TIMES

CHANGE NO.	REV.
DESIGN	D
DATE	8/7/73
BY	SH003

ETCH REV.	D1-P1
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SIGNATURES	DATE	digital
DRN. D. DINGELDEIN	11/27/73	
CHK. D. [Signature]	27-6-73	
MECH. ENC. [Signature]	8-24-73	
PROJ. ENC. [Signature]	8-24-73	TITLE
PROD. [Signature]	8-24-73	MSII-P MEMORY
SCALE 2/1		SIZE CODE
SHT. OF		NUMBER
NEXT HIGHER ASSY. B-DD-M8743-BA		D UA M8743-BA-0

SHO 1 MS#



STARTING ADDRESS DECIMAL (K) OCTAL	SWITCH SETTINGS FOR E38			
	PA17	PA16	PA15	PA14
0 0000000				
8 0004000				X
16 0010000			X	
24 0014000			X	X
32 0020000		X		
40 0024000		X		X
48 0030000		X	X	
56 0034000		X	X	X
64 0040000	X			
72 0044000	X			X
80 0050000	X		X	
88 0054000	X		X	X
96 0060000	X	X		
104 0064000	X	X		X
112 0070000	X	X	X	
120 0074000	X	X	X	X

X = OFF (OPEN)

NOTES:  
 1. CHIPS POWER AND GND PINS ARE ALL STANDARD EXCEPT 8648 (+5VB PIN 8, GND PIN 1) AND 4164 (+5VB PIN 8, GND PIN 16). ALL RESISTORS ARE 1/4W 5% EXCEPT AS NOTED ON SCHEMATIC.  
 2. \* E52 AND E53 ARE FUTURE OPTION.

STARTING ADDRESS RANGE DECIMAL (K) OCTAL	SWITCH SETTINGS FOR E38			
	PA21	PA20	PA19	PA18
128-248 01000000-01740000				X
256-376 02000000-02740000			X	
384-504 03000000-03740000			X	X
512-632 04000000-04740000		X		
640-760 05000000-05740000		X		X
768-888 06000000-06740000		X	X	
896-1016 07000000-07740000		X	X	X
1024-1144 10000000-10740000	X			
1152-1272 11000000-11740000	X			X
1280-1400 12000000-12740000	X		X	
1408-1528 13000000-13740000	X		X	X
1536-1656 14000000-14740000	X	X		
1664-1784 15000000-15740000	X	X		X
1792-1912 16000000-16740000	X	X	X	
1920-2040 17000000-17740000	X	X	X	X

TO OBTAIN INCREMENTAL 8K BOUNDARIES WITHIN THE SPECIFIED RANGE, REPEAT SWITCH SETTINGS FROM 0-128K TABLE.  
 X = OFF (OPEN)

CSR STARTING ADDRESS OCTAL	SWITCH SETTINGS FOR E28			
	CSRA3	CSRA2	CSRA1	CSRA0
17772'00				
17772'02				X
17772'04			X	
17772'06			X	X
17772'10		X		
17772'12		X		X
17772'14		X	X	
17772'16		X	X	X
17772'20	X			
17772'22	X			X
17772'24	X		X	
17772'26	X		X	X
17772'30	X	X		
17772'32	X	X		X
17772'34	X	X	X	
17772'36	X	X	X	X

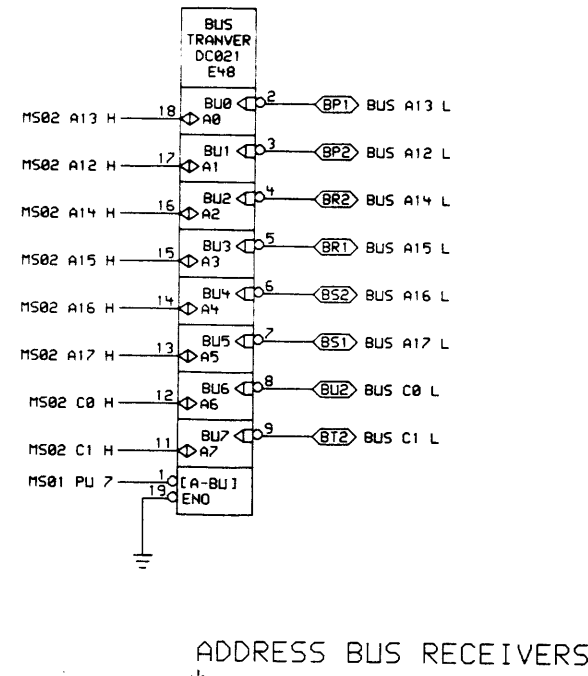
X = OFF (OPEN)

POWER DISTRIBUTION

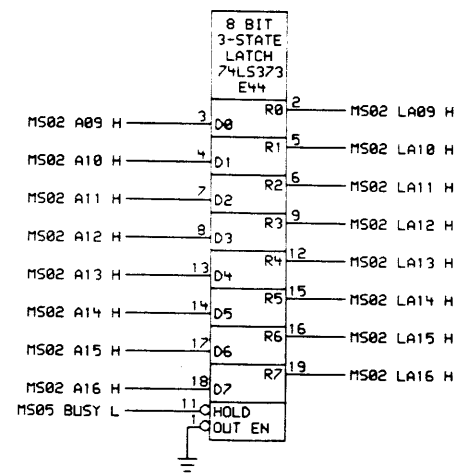
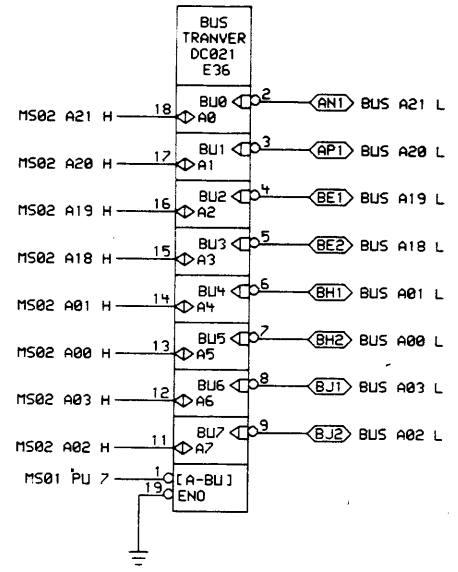
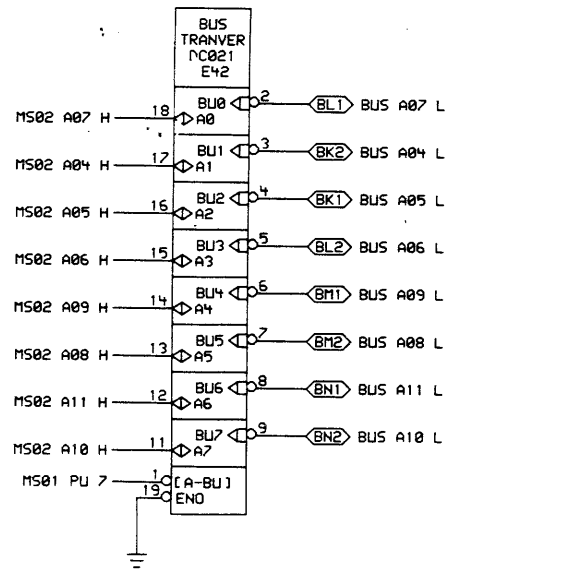
SWITCH CONFIGURATION

REVISIONS		
CHK	CHANGE NO.	REV

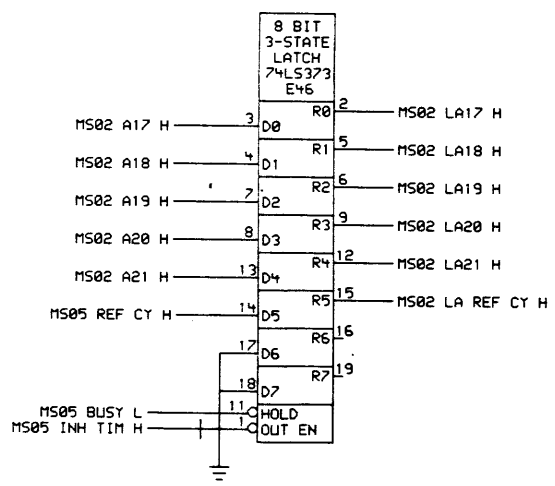
digital	DATE ENG	DATE	TITLE:	MS11-P MEMORY
	05-JAN-83	05-JAN-83		
CHK'D.	DATE BOARD	LOCATION:	SIZE CODE	NUMBER
	05-JAN-83		K CS	M8743-BA-MS01
FIRST USED ON OPTION/MODEL: MS11-P B-DD-M8743-BA			REV.	B



ADDRESS BUS RECEIVERS



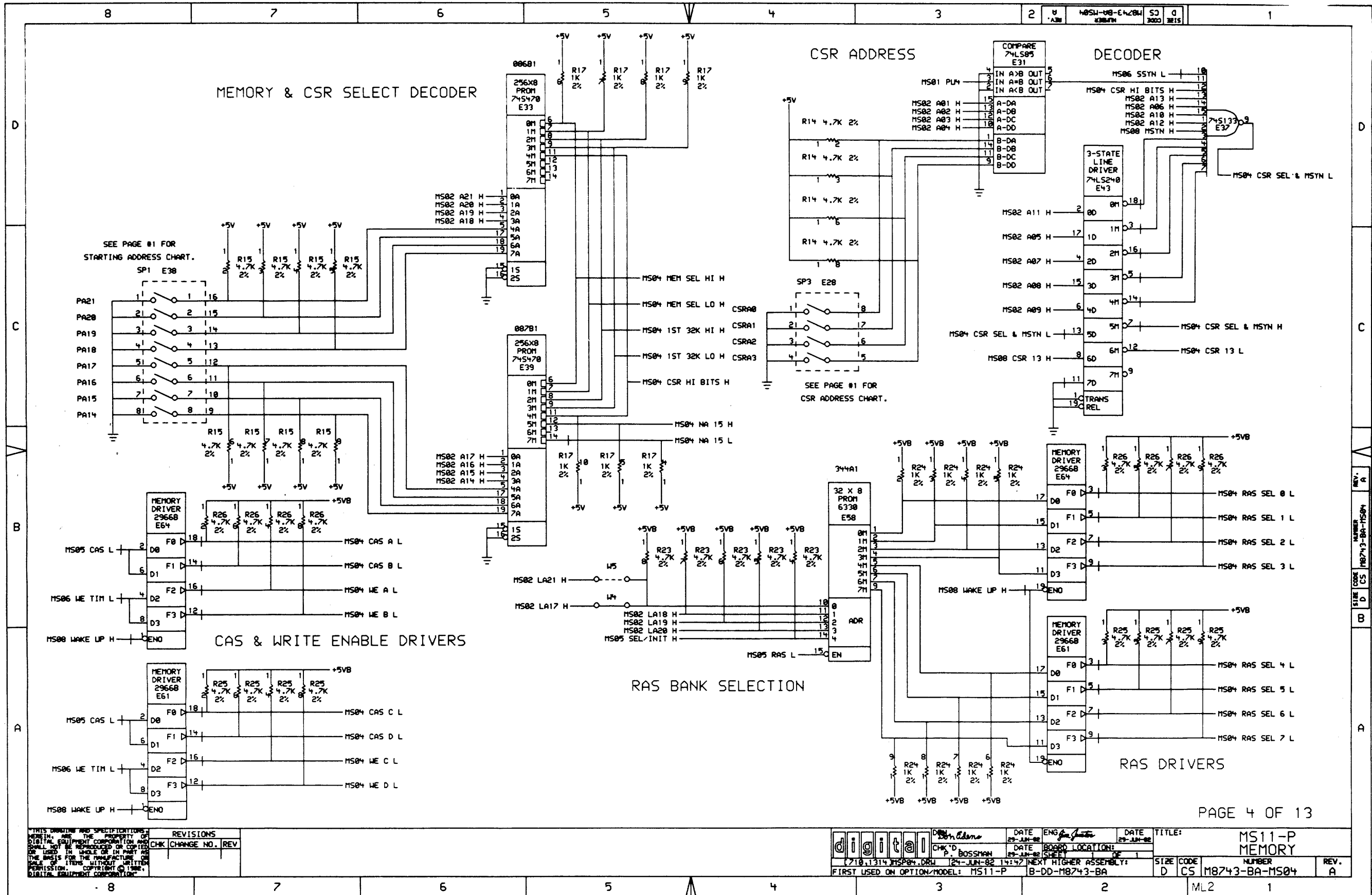
ADDRESS LATCHES



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REVISIONS		
CHK	CHANGE NO.	REV

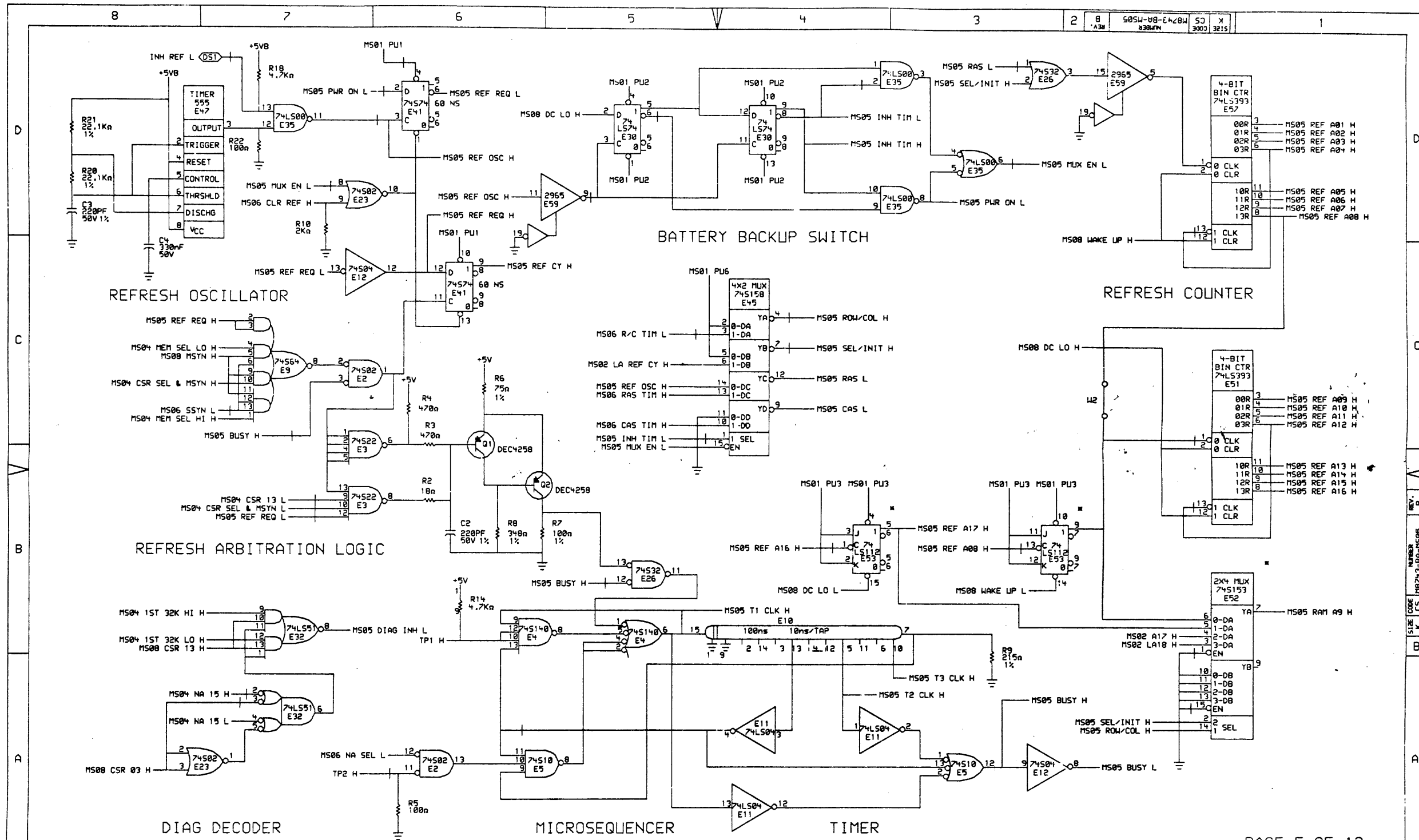
digital	CHK'D	DATE	ENG	DATE	TITLE:
	P. BOSSMAN	29-JUN-82	for Jester	29-JUN-82	MS11-P MEMORY
C710.1314 JMS02.DRW		24-JUN-82 14:29	NEXT HIGHER ASSEMBLY:		SIZE CODE
FIRST USED ON OPTION/MODEL: MS11-P		B-DD-M8743-BA		D CS	NUMBER
				D CS	M8743-BA-MS02
				V.2	1



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REVISIONS	
CHK	CHANGE NO. REV

	DATE: 29-JUN-82	ENG: <i>for Jett</i>	DATE: 29-JUN-82	TITLE: MS11-P MEMORY
	DATE: 29-JUN-82	CHK'D: P. BOSSMAN	DATE: 29-JUN-82	BOARD LOCATION: SHEET 1
FIRST USED ON OPTION/MODEL: MS11-P		NEXT HIGHER ASSEMBLY: B-DD-M8743-BA		SIZE: D CS
ML2		NUMBER: M8743-BA-MS04		REV: A

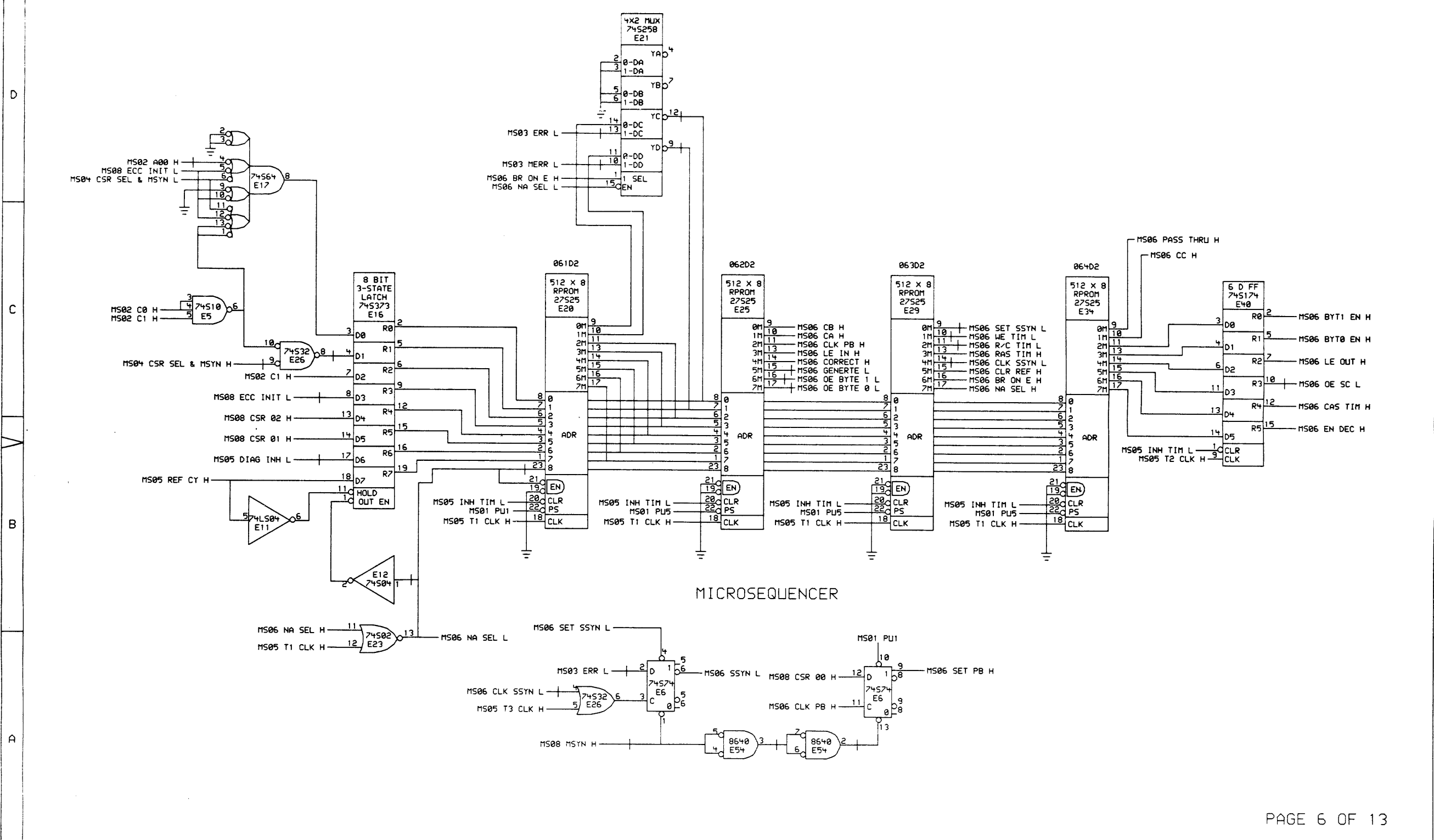


SEE NOTE #2, PAGE #1. PAGE 5 OF 13

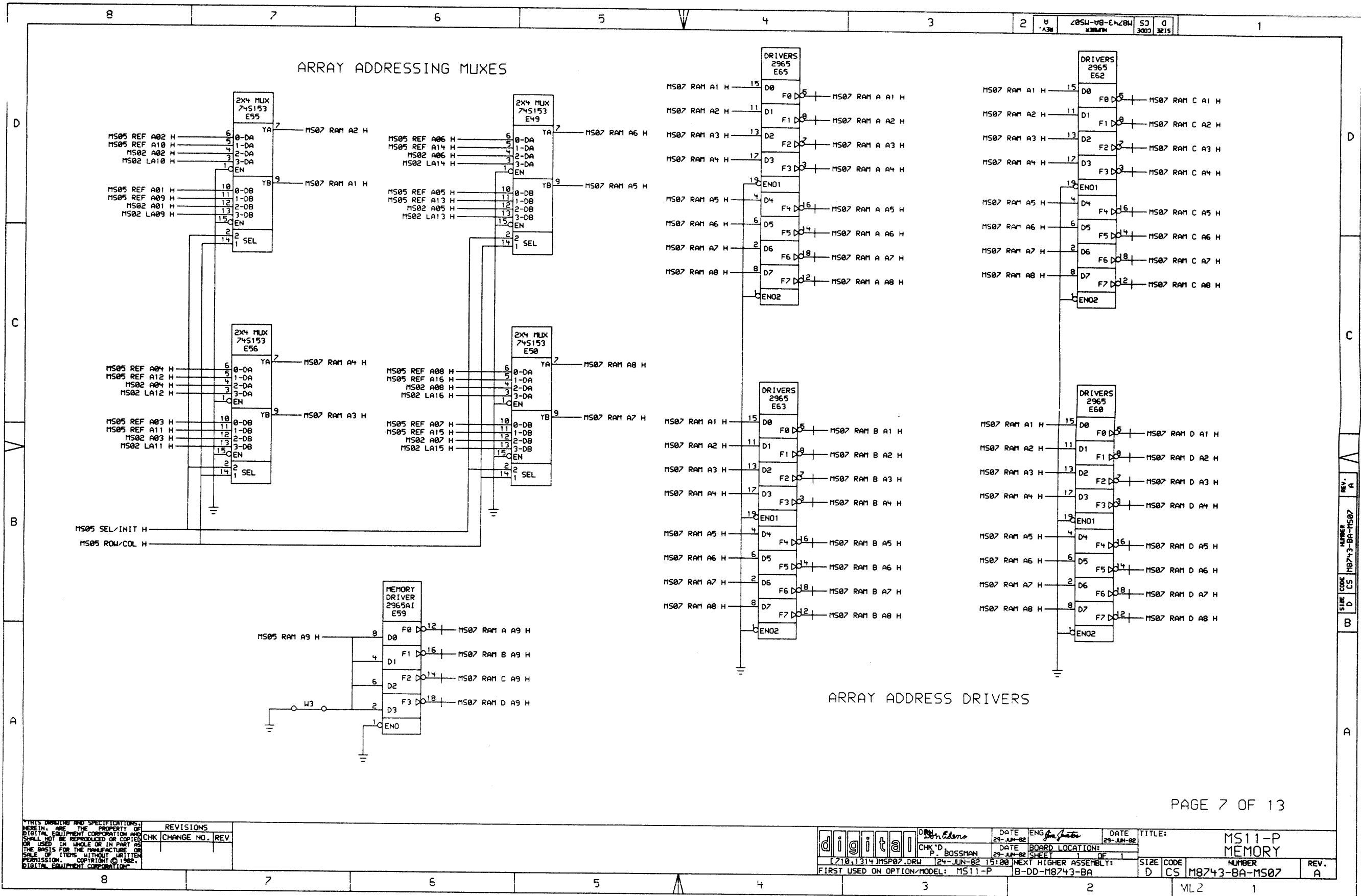
REV. NO.	DESCRIPTION
1	INITIAL DESIGN
2	REVISIONS
3	CHK CHANGE NO. REV

DATE	ENG	DATE	TITLE
29-APR-83	P. BOSSMAN	29-APR-83	MS11-P MEMORY
01-APR-83 15:24			

SIZE	CODE	NUMBER	REV.
K	CS	M8743-BA-MS05	B
SHO			



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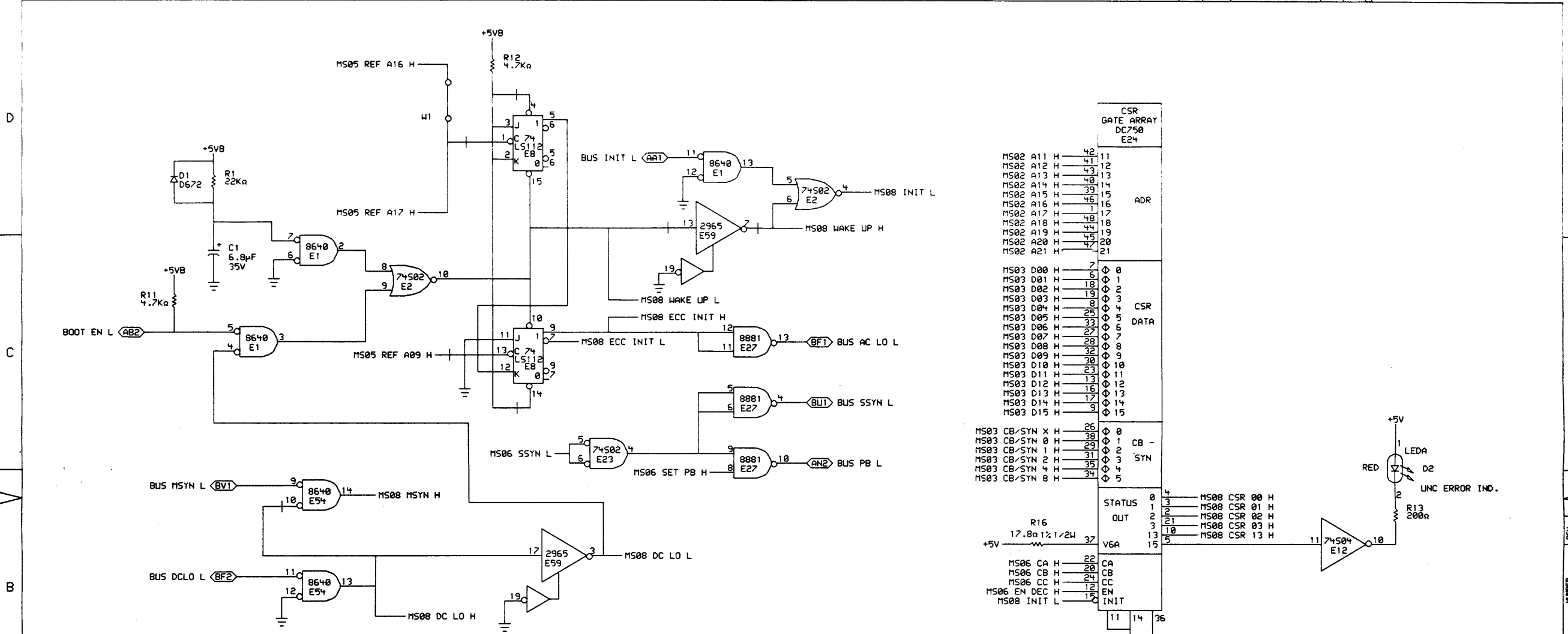
REVISIONS

CHK	CHANGE NO.	REV

digital

710,1314 MSP07.DRW 24-JUN-82 15:00 NEXT HIGHER ASSEMBLY: B-DD-M8743-BA

ML2 1



UNIBUS CONTROL LOGIC

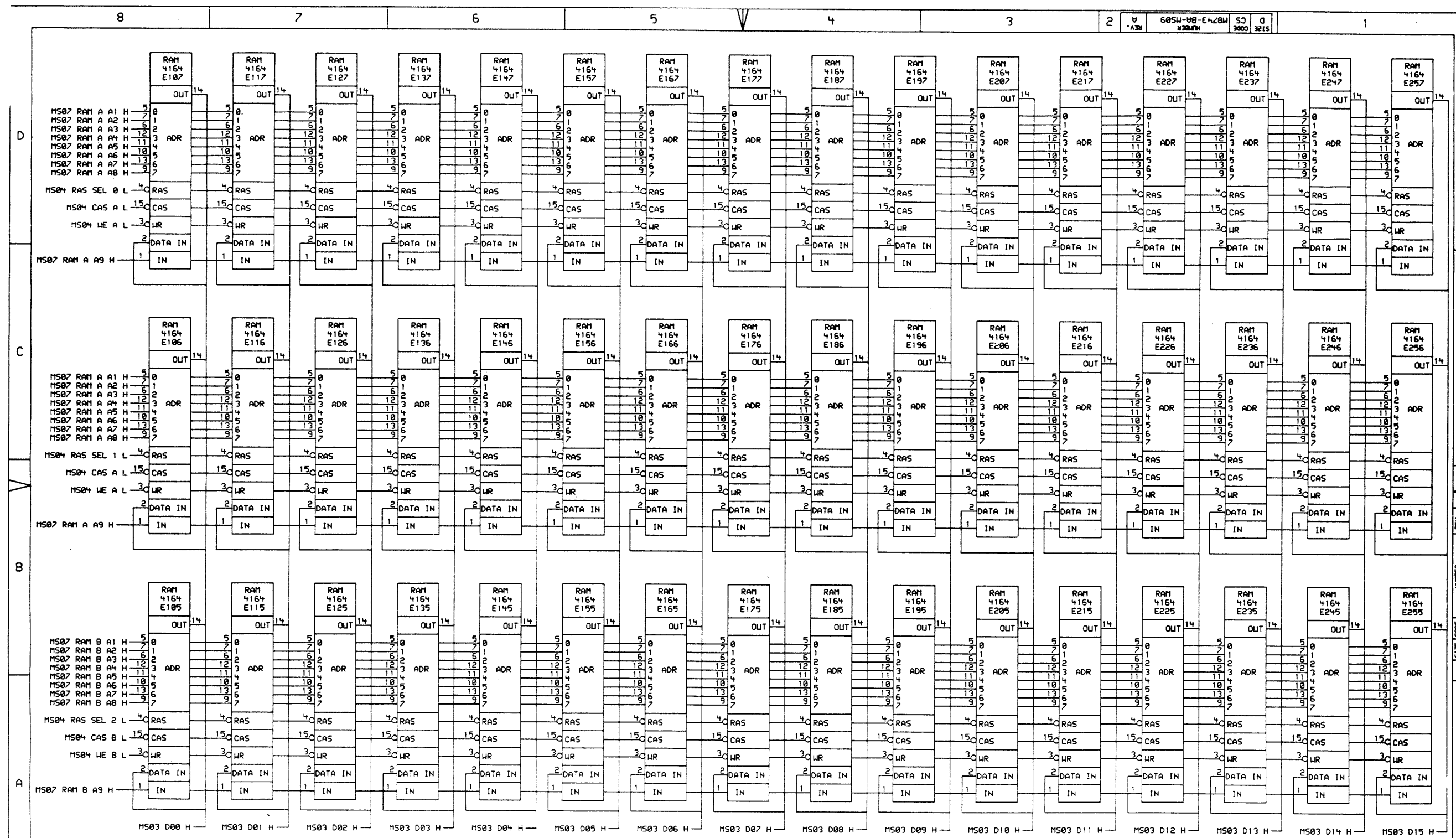
CSR LOGIC

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REVISIONS	
CHK	CHANGE NO. REV

digital	DATE	29-JUN-82	DATE	29-JUN-82	TITLE:	MS11-P
	CHK'D	BOSSMAN	DATE	29-JUN-82	BOARD LOCATION:	MEMORY
FIRST USED ON OPTION/MODEL:		MS11-P	NEXT HIGHER ASSEMBLY:		B-DD-M8743-BA	
SIZE	CODE	D	CS	NUMBER	M8743-BA-MS08	REV.
						A



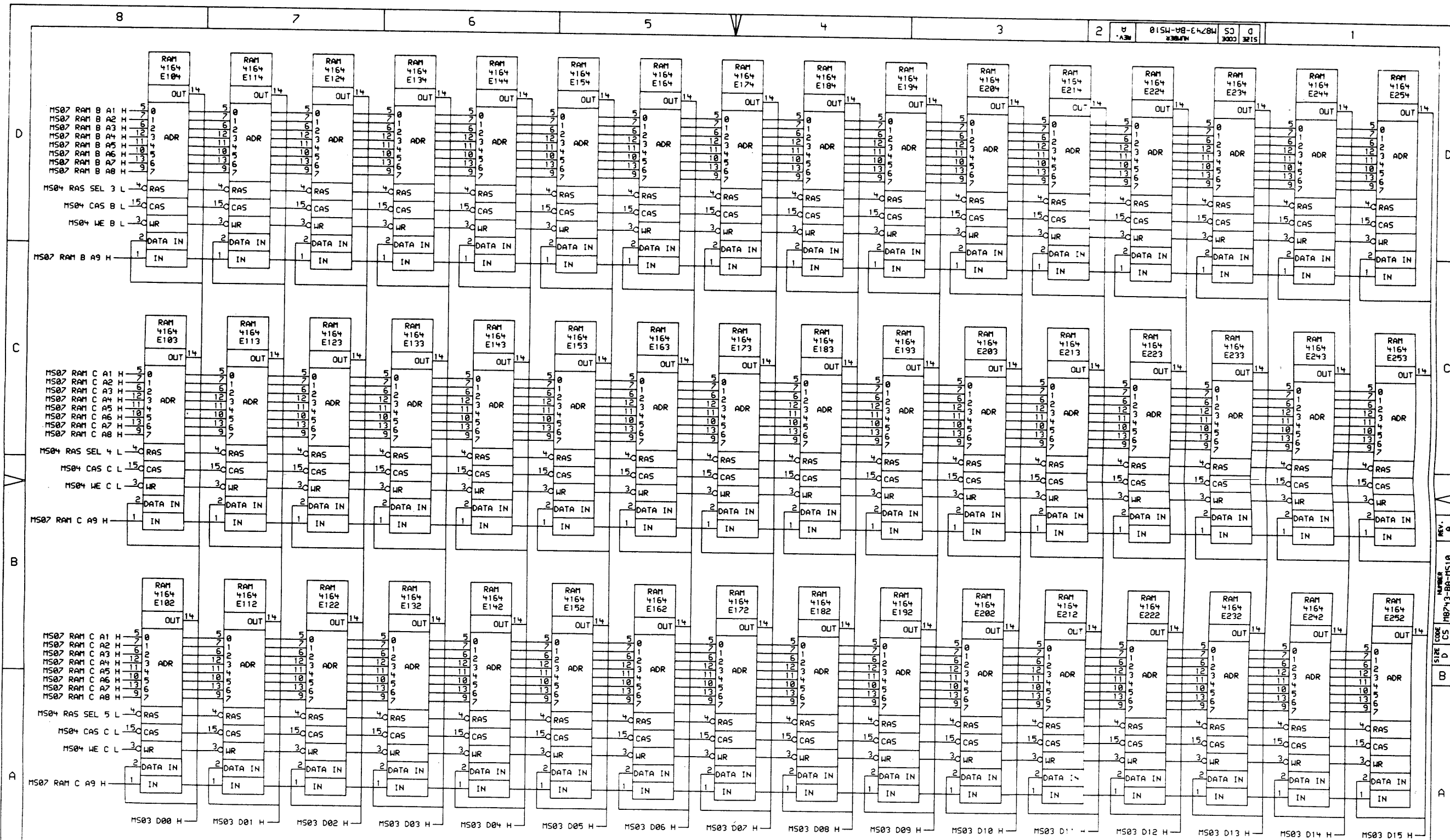


MEMORY ARRAY

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REVISIONS	
CHK	CHANGE NO. / REV

	DATE: 29-JUN-82 CHK'D: BOSSMAN DATE: 29-JUN-82	ENG: <i>John</i> DATE: 29-JUN-82 BOARD LOCATION: OF 1	TITLE: MS11-P MEMORY
	(718,1314) MS09.DRW FIRST USED ON OPTION MODEL: MS11-P	NEXT HIGHER ASSEMBLY: B-DD-M8743-BA	SIZE: D CODE: CS



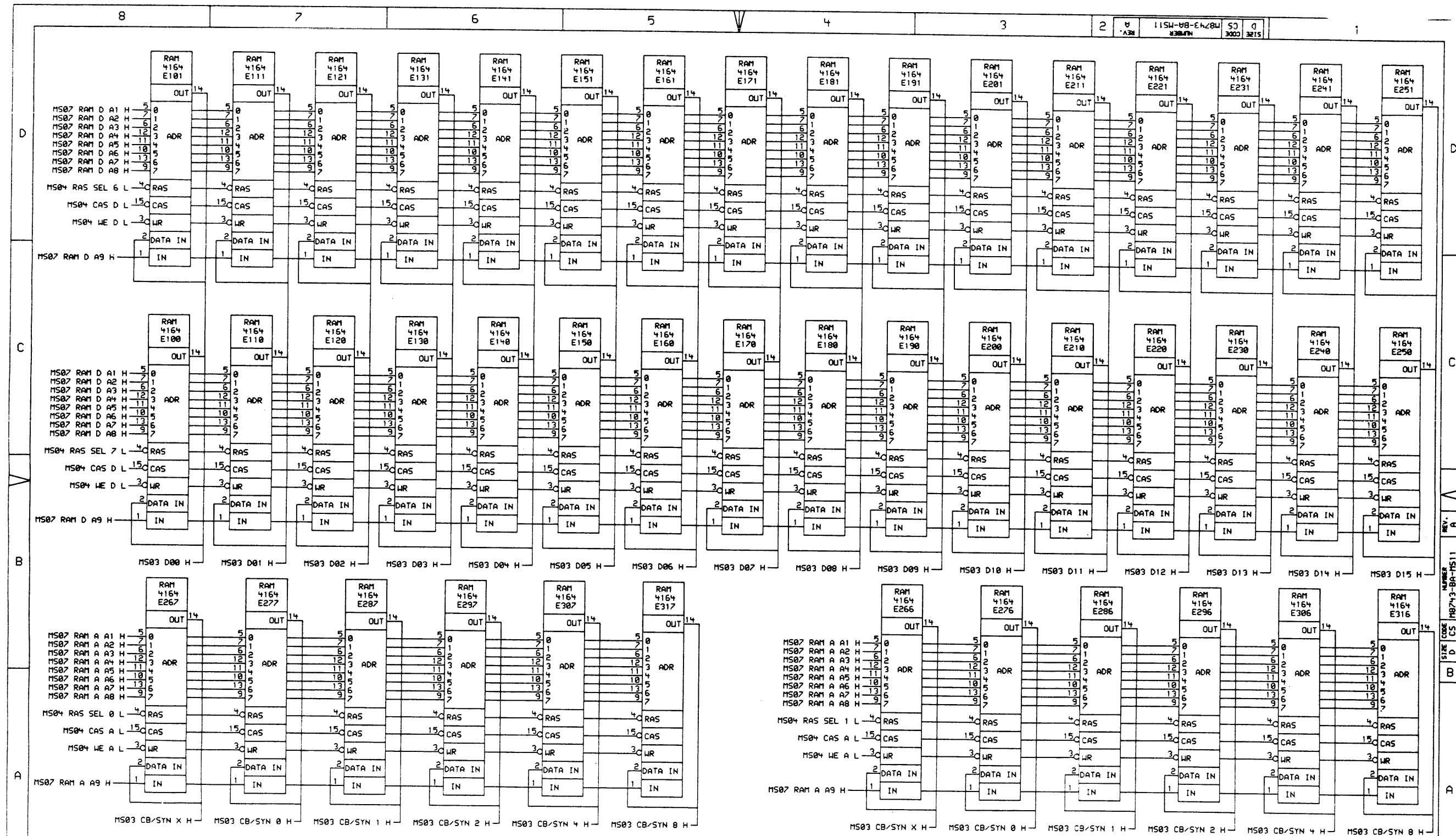
MEMORY ARRAY

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REVISIONS	
CHK	CHANGE NO. REV

digital	DRN	DATE	ENG	DATE	TITLE:
	W. BOSSMAN	25-JUN-82	J. JONES	29-JUN-82	MS11-P MEMORY
CHK'D		DATE	BOARD LOCATIONS:	SIZE CODE NUMBER	
P. BOSSMAN		29-JUN-82	SHEET 1 OF 1	D CS M8743-BA-MS10	
FIRST USED ON OPTION/MODEL: MS11-P			NEXT HIGHER ASSEMBLY: B-DD-M8743-BA		

REV. A  
SIZE CODE CS M8743-BA-MS10



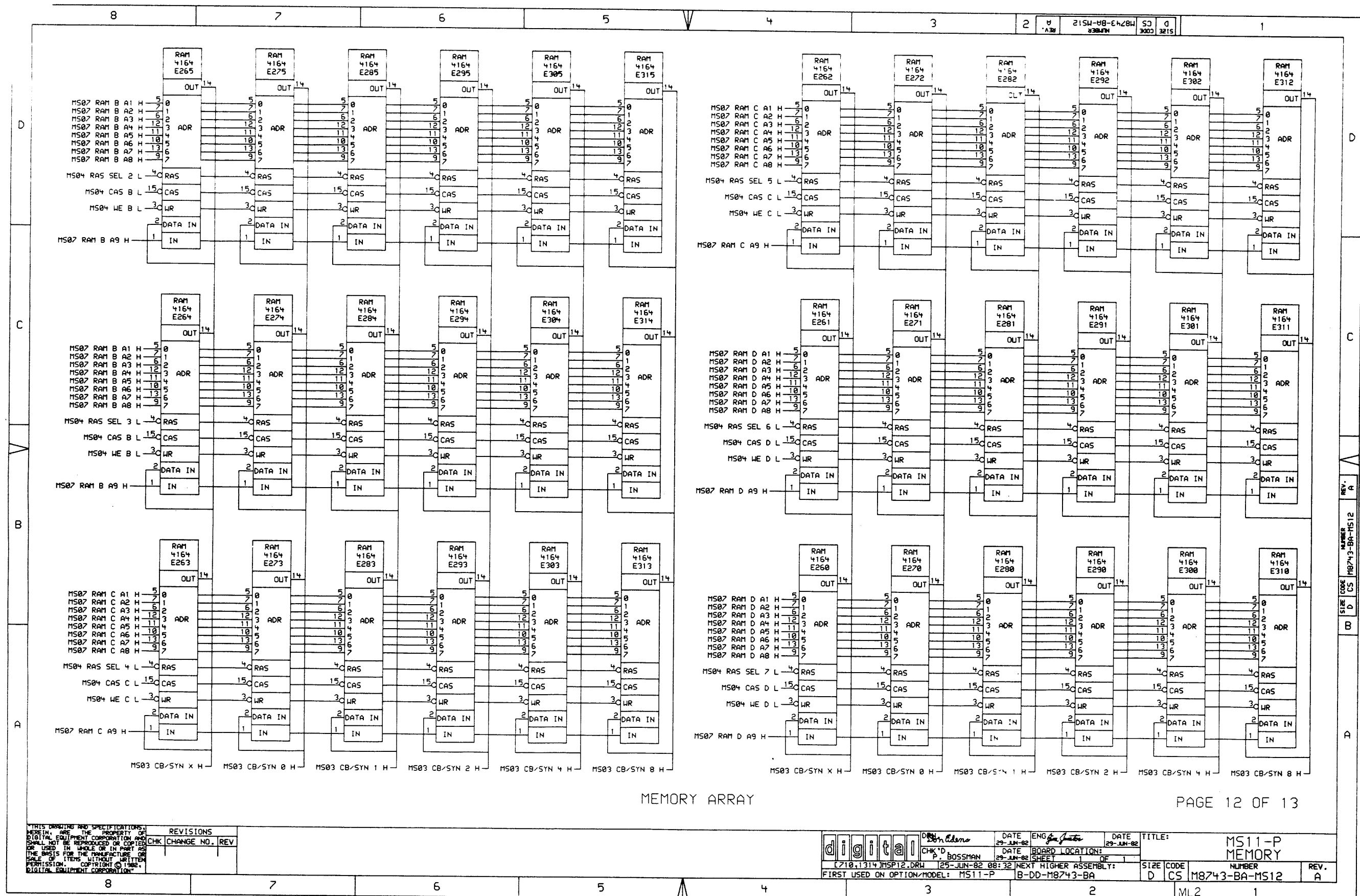
MEMORY ARRAY

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REVISIONS
CHK CHANGE NO. REV

	DATE: 29-JUN-82	ENG: <i>Jim Jones</i>	DATE: 29-JUN-82	TITLE: MS11-P MEMORY
	CHK'D: P. BOSSMAN	DATE: 29-JUN-82	SHEET: 1 OF 1	REV: A
FIRST USED ON OPTION MODEL: MS11-P			DATE: 29-JUN-82	TIME: 08:24
NEXT HIGHER ASSEMBLY: B-DD-M8743-BA			SIZE CODE: D CS	NUMBER: M8743-BA-MS11

REV. A  
NUMBER CS  
M8743-BA-MS11  
SIZE D

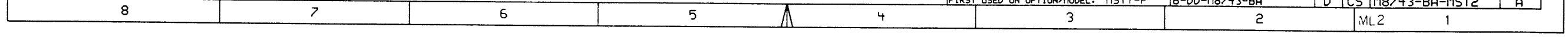


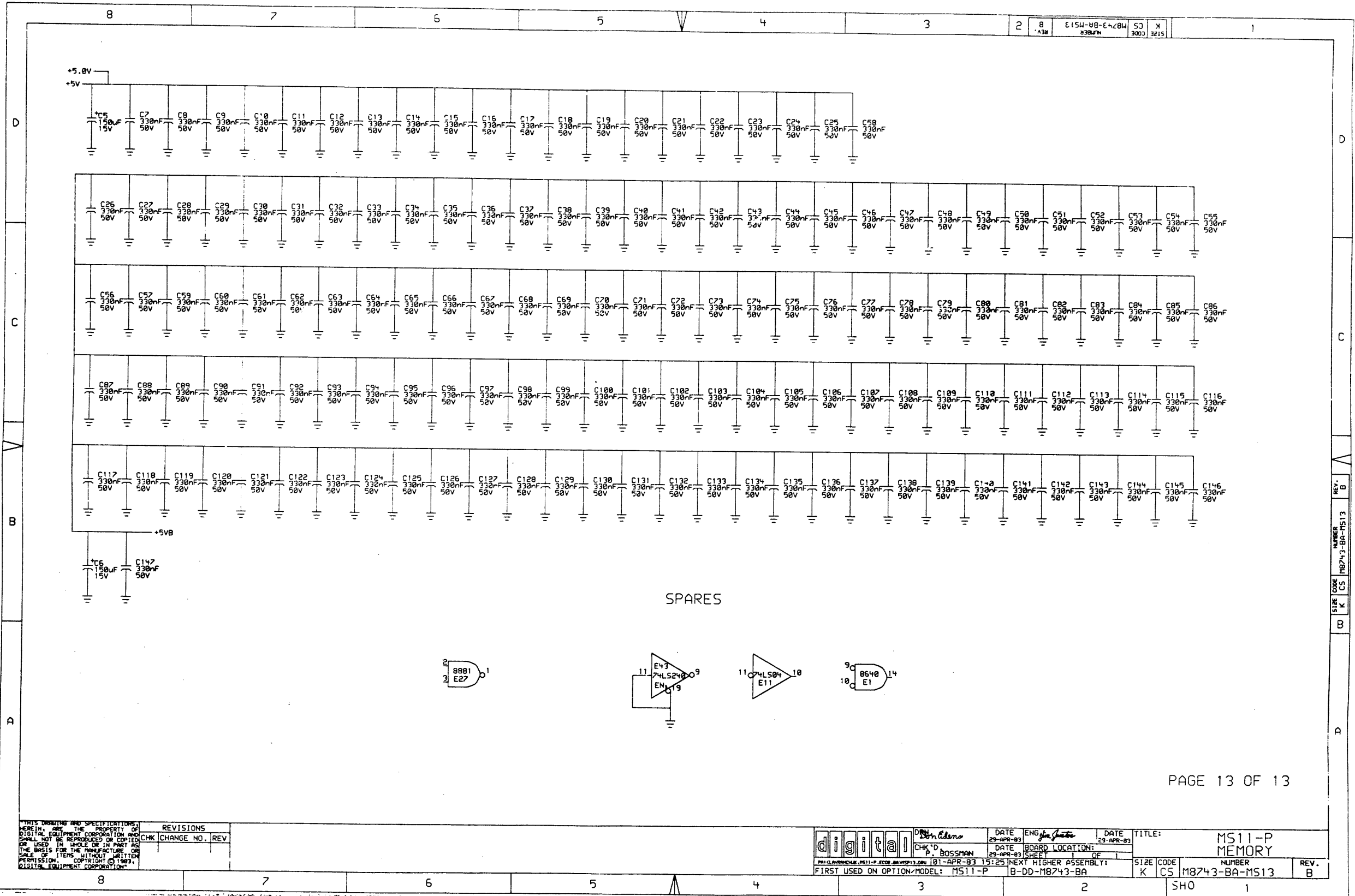
MEMORY ARRAY

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REVISIONS		
CHK	CHANGE NO.	REV

	DATE: 29-JUN-82 ENG: <i>for Jester</i>	DATE: 29-JUN-82 TITLE: MS11-P MEMORY
	CHK'D: P. BOSSMAN DATE: 29-JUN-82 08:32 BOARD LOCATION: B-DD-M8743-BA SHEET 1 OF 1	SIZE: D CODE: CS NUMBER: M8743-BA-MS12






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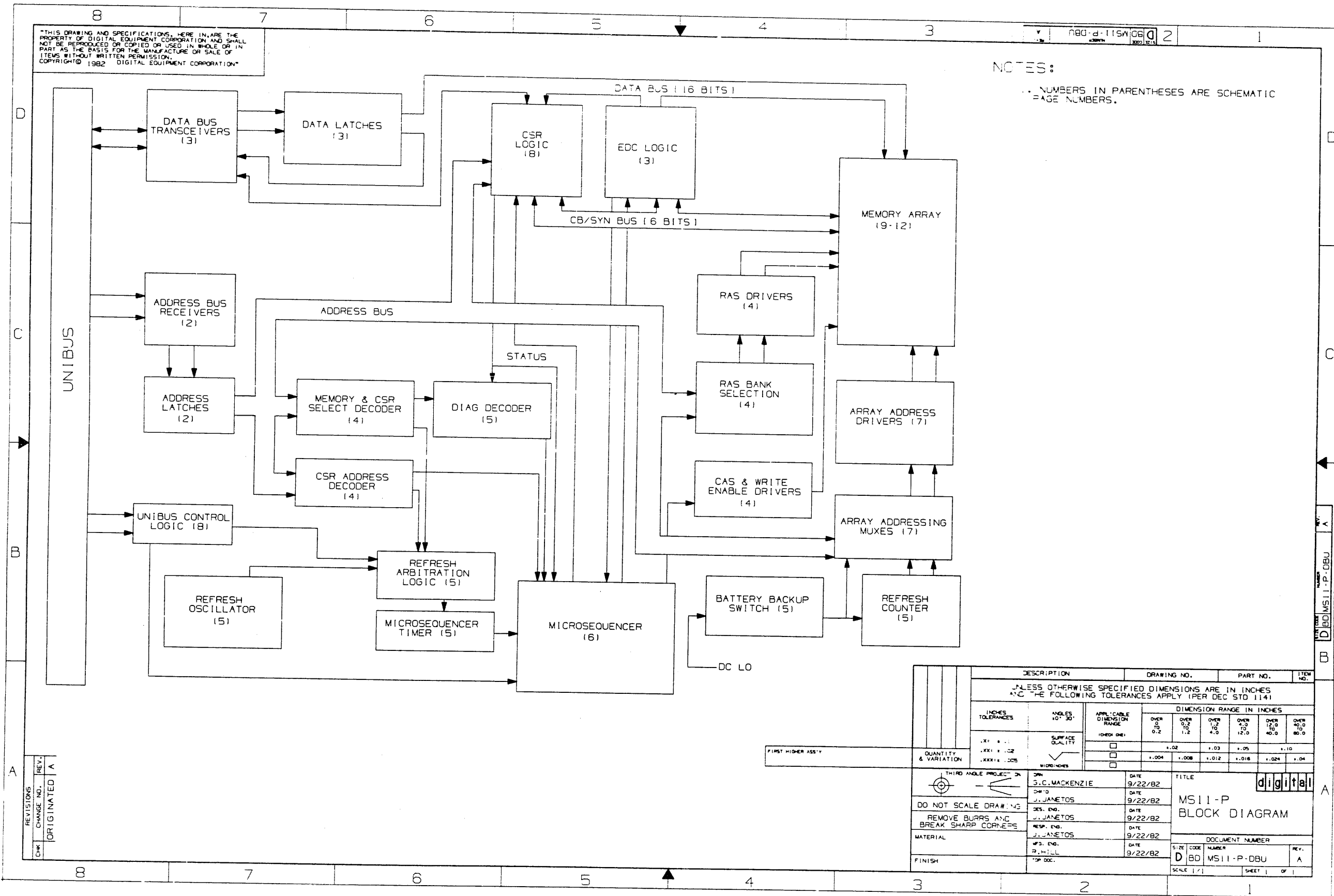
REVISIONS	
CHK	CHANGE NO. REV

	DATE: 29-APR-83 ENG: J. J. J.	DATE: 29-APR-83 TITLE: MS11-P MEMORY
	CHK'D: P. BOSSMAN DATE: 29-APR-83 BOARD LOCATION: 1	DATE: 29-APR-83 SHEET: 1
FIRST USED ON OPTION/MODEL: MS11-P		NEXT HIGHER ASSEMBLY: B-DD-M8743-BA

DRAWING NO.	NO. OF SHTS.	PART NO.	DESCRIPTION	REVISIONS																		
				REF																		
B-TC-MS11-P-1	-	MP01477	FIELD MAINTENANCE PRINT SET	REF																		
		MS11-PB	512 X 22 MOS MEMORY SYSTEM	A																		
		M8743-BA	MS11-P MOS MEMORY	A																		
B-DD-M8743-BA	1		MS11-P MOS MEMORY	A																		
D-BD-MS11-P-DBU	1		MS11-P BLOCK DIAGRAM	A																		
D-TD-MS11-P-DBU	6		MS11-P TIMING DIAGRAM	A																		
E-FD-MS11-P-3	1		MS11-P FLOW DIAGRAM	A																		
A-SP-MS11-P-2	29		MS11-P SPECIFICATION	A																		
K-PL-MS11-P-DBP1	1		MS11-P SHIPPING LIST	A																		
<b>NOTES:</b>				REV.																		
				ECO NO.																		
				DATE	9/82																	
				INIT.																		
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NOTES:  
 .. NUMBERS IN PARENTHESES ARE SCHEMATIC PAGE NUMBERS.

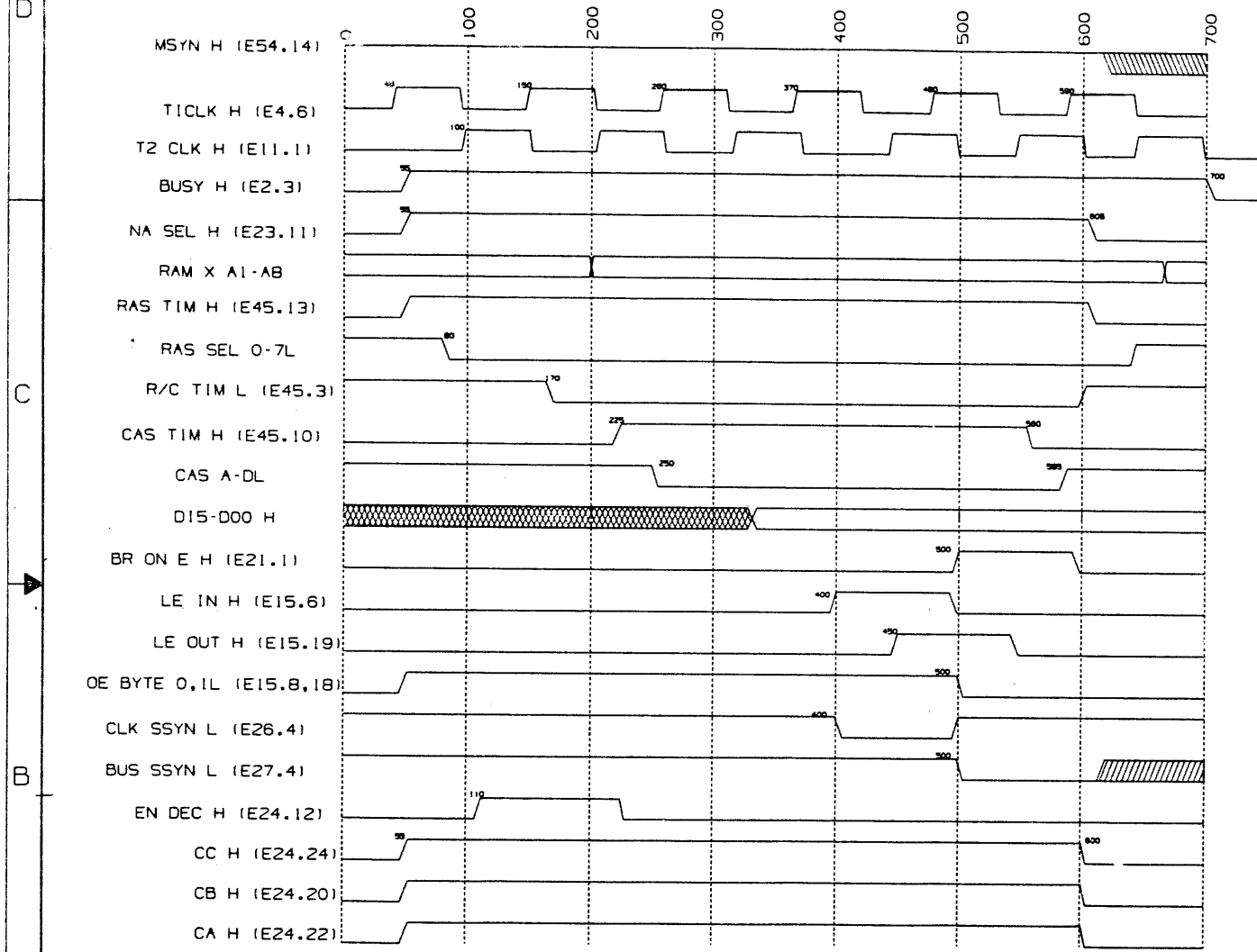


REVISIONS	REV.	ORIGINATED
CHANGE NO.		
CHECK		

DESCRIPTION		DRAWING NO.	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 114)				
INCHES TOLERANCES	ANGLES 10'-30'	APPLICABLE DIMENSION RANGE	DIMENSION RANGE IN INCHES	
.XX ± .01		(CHECK ONE)	OVER 0 TO 0.2	OVER 0.2 TO 1.2
.XXX ± .02		<input type="checkbox"/>	OVER 1.2 TO 4.0	OVER 4.0 TO 12.0
.XXXX ± .05		<input type="checkbox"/>	OVER 12.0 TO 40.0	OVER 40.0 TO 80.0
	SURFACE QUALITY	<input type="checkbox"/>	±.02	±.05
	MICRO-INCHES	<input type="checkbox"/>	±.004	±.008
		<input type="checkbox"/>	±.012	±.018
		<input type="checkbox"/>	±.024	±.04
THIRD ANGLE PROJECTION	DATE	TITLE		
DO NOT SCALE DRAWING	9/22/82	MS11-P BLOCK DIAGRAM		
REMOVE BURRS AND BREAK SHARP CORNERS	9/22/82	DOCUMENT NUMBER		
MATERIAL	9/22/82	SIZE CODE NUMBER REV.		
FINISH	9/22/82	D BD MS11-P-DBU A		
		SCALE 1/1 SHEET 1 OF 1		

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Y TD MS11-P-DBU 2 1



(DATA, TIMING)

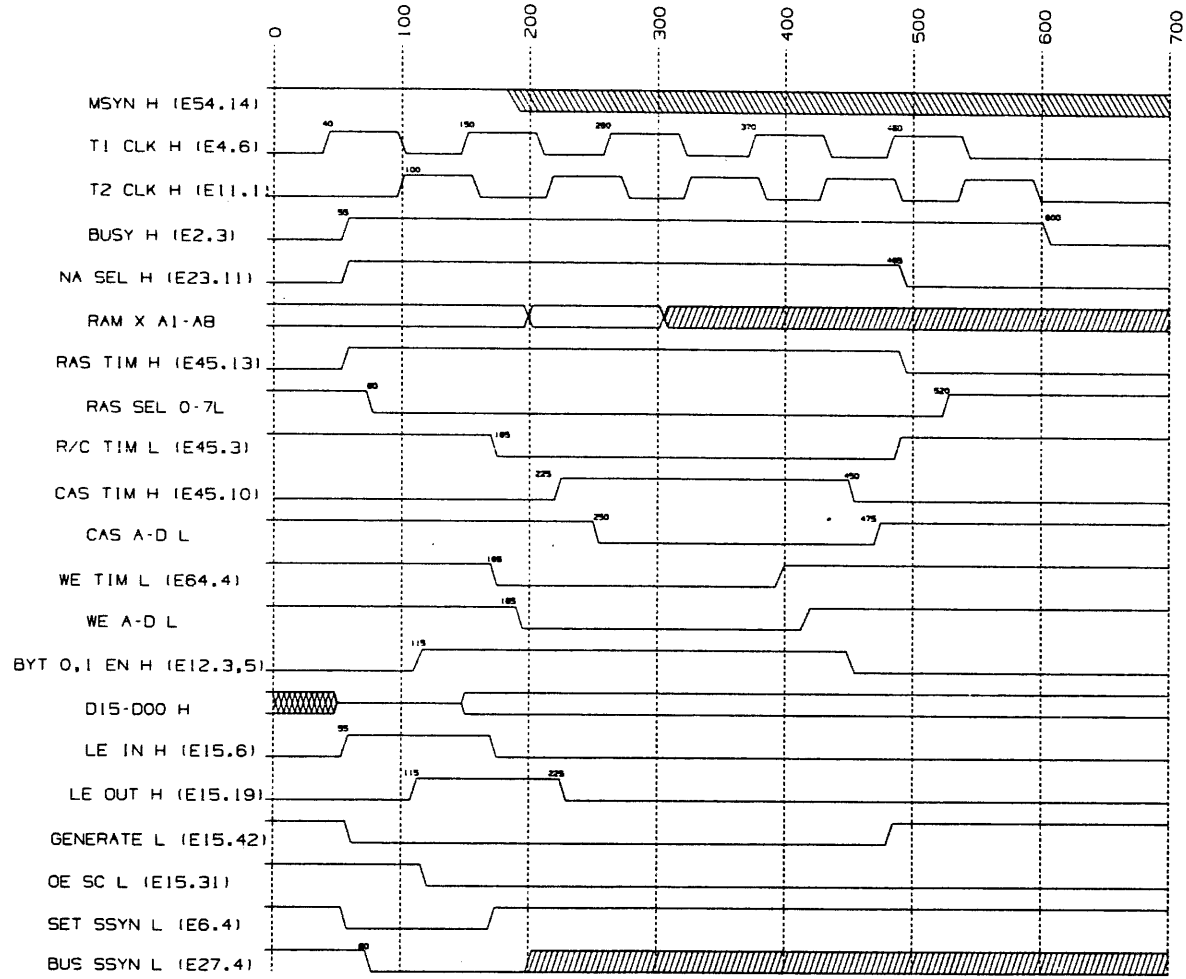
REVISIONS	REV.	A
CHK	CHANGE NO.	
	ORIGINATED	A

DESCRIPTION		DRAWING NO.	PART NO.	ITEM NO.
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 114)				
INCHES TOLERANCES	ANGLES 10°-90°	DIMENSION RANGE IN INCHES		
.X1 ± .1	SURFACE QUALITY	APPLICABLE DIMENSION RANGE	OVER 12.0 TO 4.0	OVER 12.0 TO 40.0
.XX ± .02		1.000-1.001	1.000-1.001	1.000-1.001
.XXX ± .005		1.000-1.001	1.000-1.001	1.000-1.001
QUANTITY & VARIATION	THIRD ANGLE PROJECTION	DRW C.W. FIELD	DATE 9/8/82	TITLE digital
	DO NOT SCALE DRAWING	CHK'D J. JANETOS	DATE 9/8/82	MEMORY SYSTEM TIMING
	REMOVE BURRS AND BREAK SHARP CORNERS	DES. ENG. J. JANETOS	DATE 9/8/82	
	MATERIAL	RESP. ENG. J. JANETOS	DATE 9/8/82	DOCUMENT NUMBER
	FINISH	MFG. ENG. R. HILL	DATE 9/8/82	SIZE CODE D
		TOP DOC.		NAME MS11-P-DBU
				REV. A
				SCALE NONE
				SHEET 1 OF 6



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MS11-P-DBU 2



(DATA TIMING)

REVISIONS		
CHK	CHANGE NO	REV

TITLE	MEMORY SYSTEM	SIZE CODE	D	NUMBER	MS11-P-DBU	REV	A
SCALE	NONE	SHEET	2	OF	6	DIST	

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DITD MS11-P-DBU 2

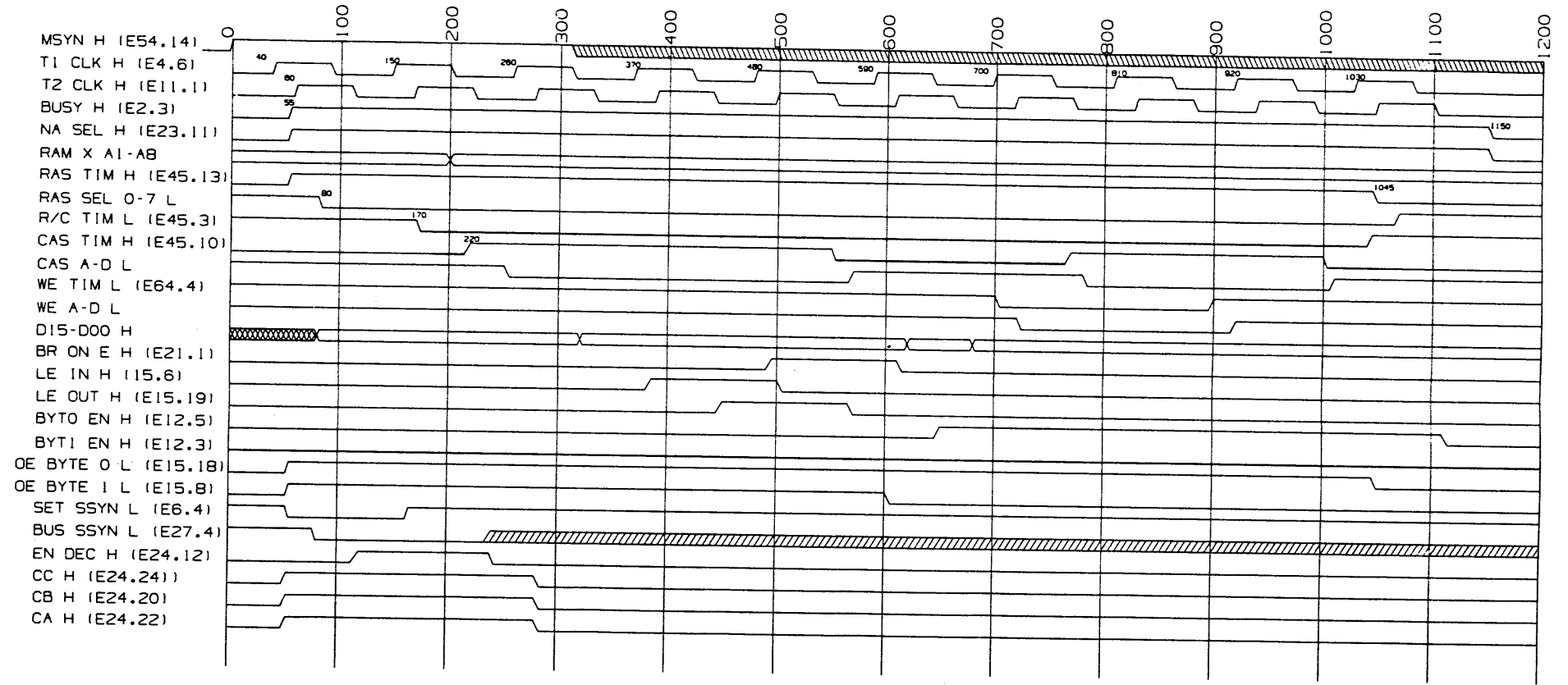
DITD MS11-P-DBU

A

C

B

A



(DATOB TIMING)

REVISIONS		
CHK	CHANGE NO	REV

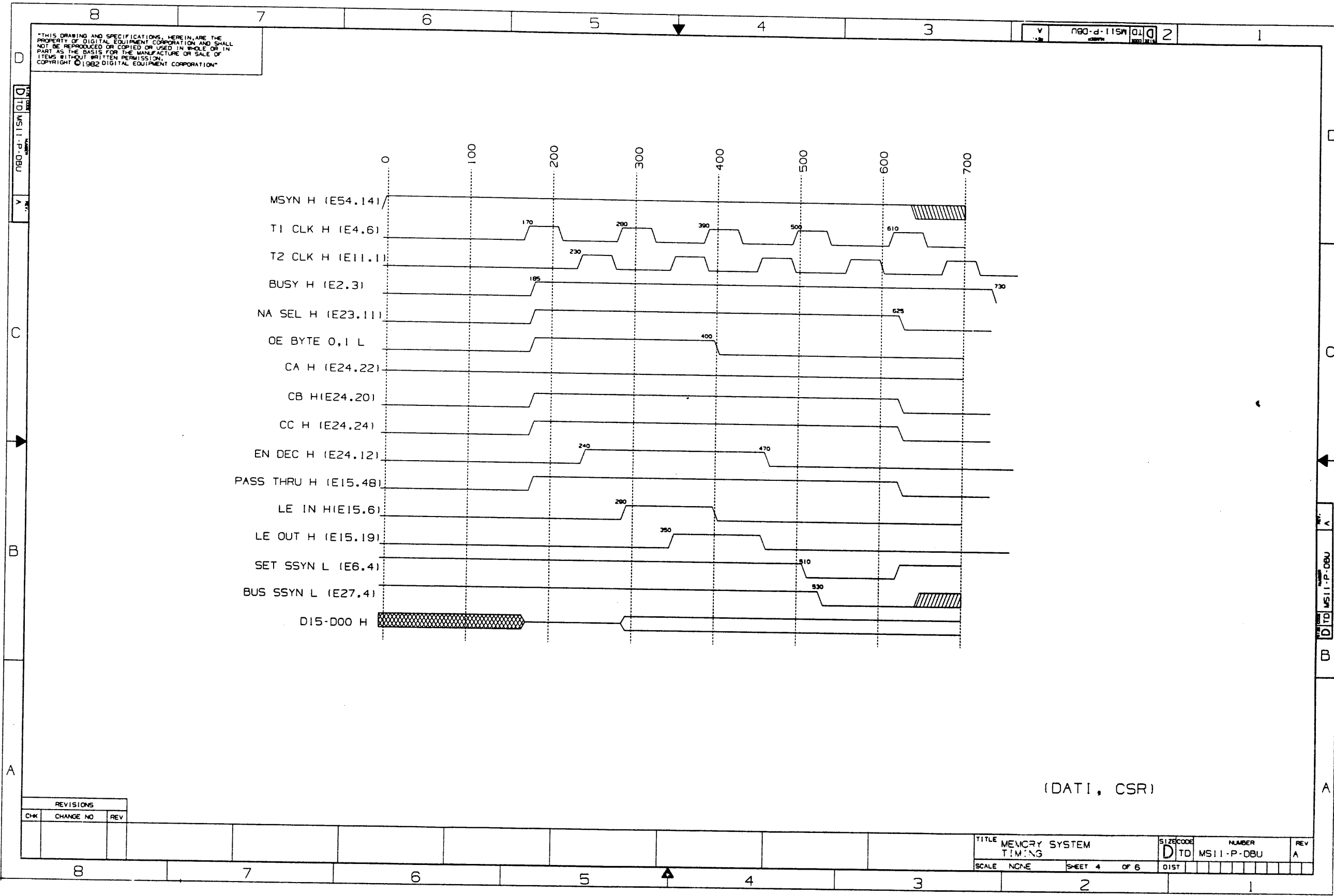
TITLE	MEMORY SYSTEM	SIZE/CODE	DITD	NUMBER	MS11-P-DBU	REV	A
SCALE	NONE	SHEET	3	OF	6	DIST	

DITD MS11-P-DBU

B

A

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(DAT1, CSR)

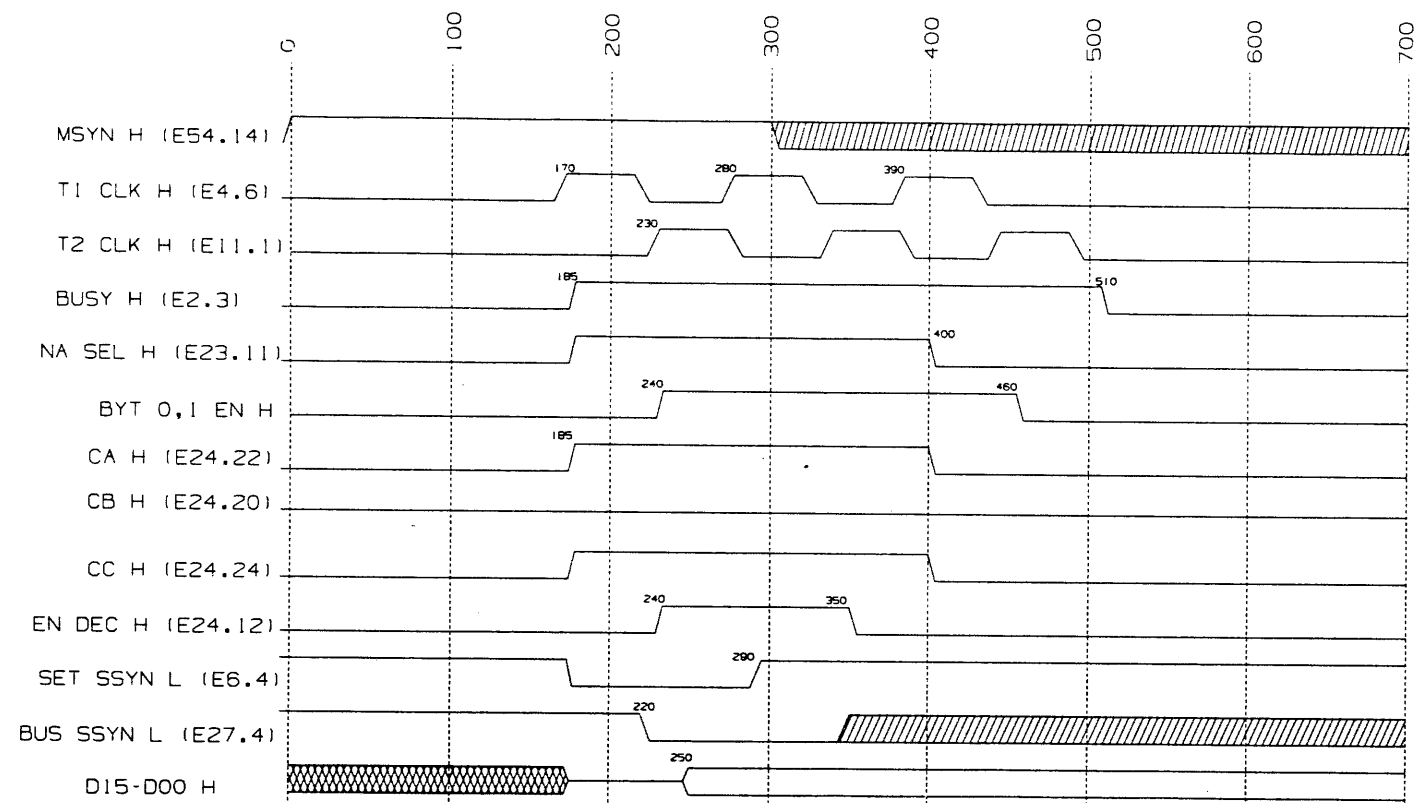
REVISIONS		
CHK	CHANGE NO	REV

TITLE MEMCRY SYSTEM TIMING		SIZE CODE D TD	NUMBER MS11-P-DBU	REV A
SCALE NONE	SHEET 4 OF 6	01ST		

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MS11-P-DBU 2

MS11-P-DBU



(DATO, CSR)

REVISIONS		
CHK	CHANGE NO	REV

TITLE MEMCRY SYSTEM TIMING		SIZE CODE D TD	NUMBER MS11-P-DBU	REV A
SCALE NONE	SHEET 5	OF 6	DIST	

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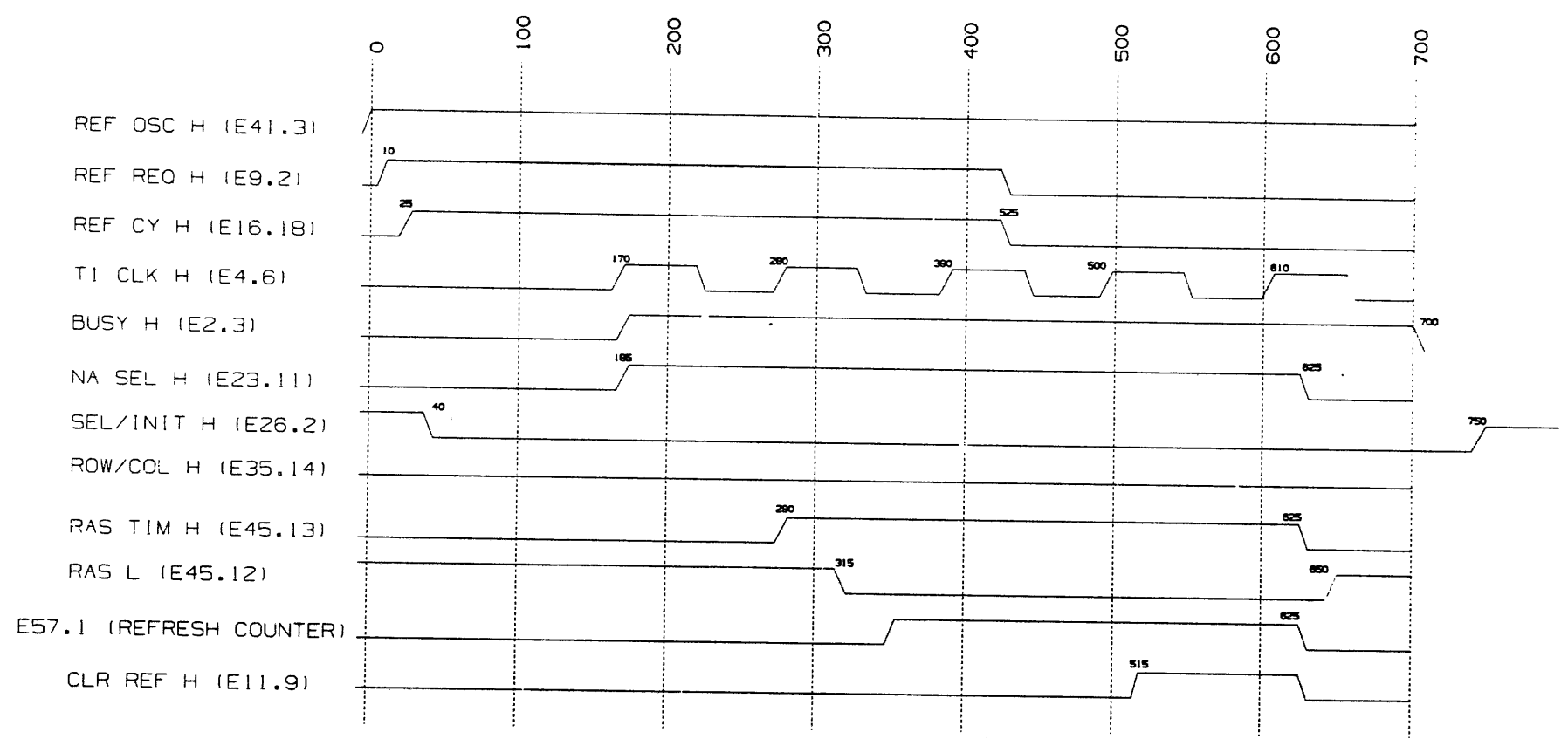
MS11-P-DBU 2

MS11-P-DBU

C

B

A



(REFRESH TIMING)

REVISIONS		
CHK	CHANGE NO	REV

TITLE MEMORY SYSTEM  
TIMING  
SCALE NONE SHEET 6 OF 6  
SIZE CODE D TD  
NUMBER MS11-P-DBU  
REV A

8

7

6

5

4

3

2

1

A

C

D

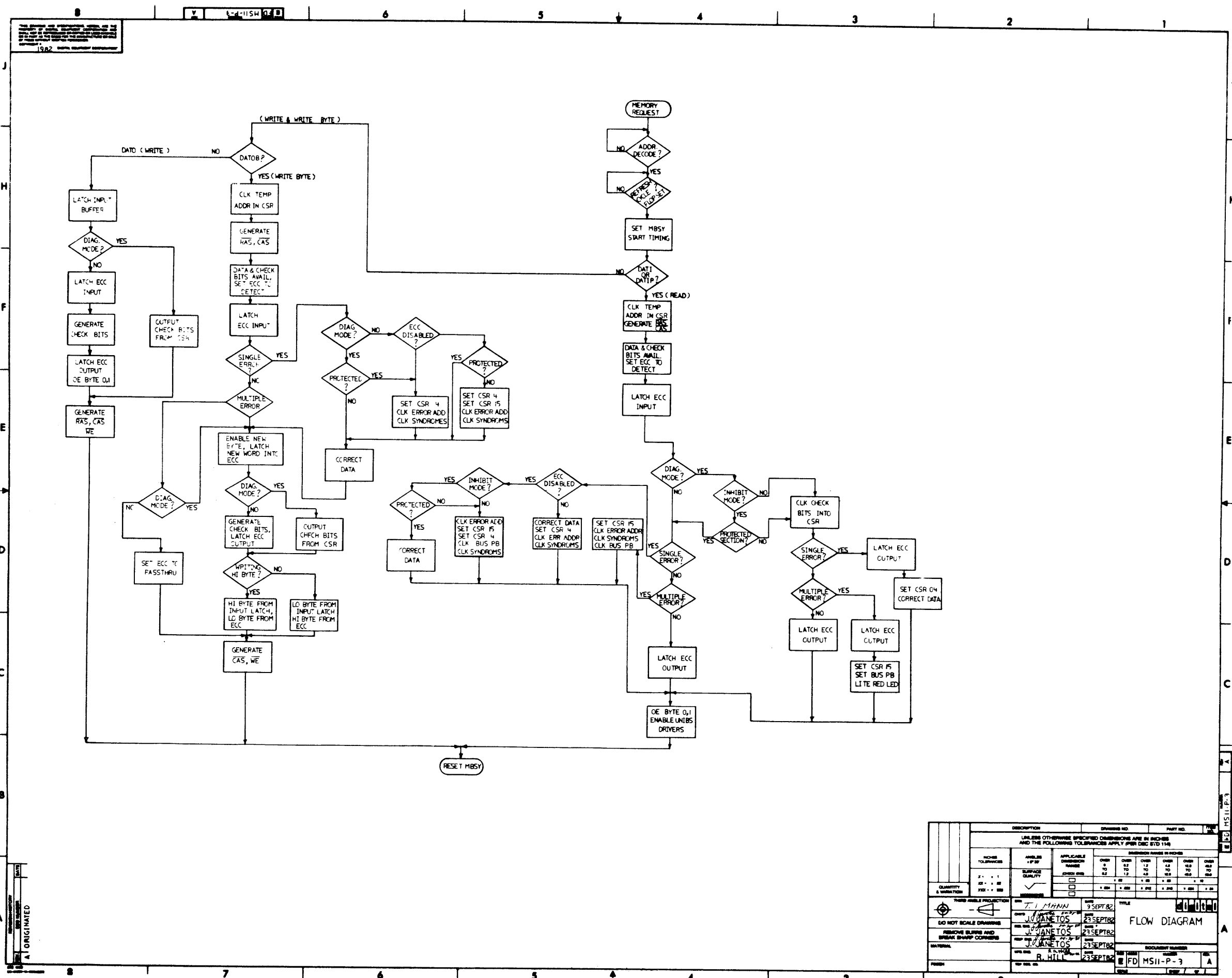
A

B

A

C

D



DESCRIPTION		DRAWING NO.		PART NO.	
UNLESS OTHERWISE SPECIFIED DIMENSIONS ARE IN INCHES AND THE FOLLOWING TOLERANCES APPLY (PER DEC STD 114)					
HIDDEN TOLERANCES	APPLICABLE DIMENSION RANGE	OVER 0 TO 0.2	OVER 0.2 TO 0.5	OVER 0.5 TO 1.0	OVER 1.0 TO 10.0
	SURFACE QUALITY	AS SHOWN	AS SHOWN	AS SHOWN	AS SHOWN
QUANTITY & VARIATION	DATE	BY	DATE	BY	DATE
THIRD ANGLE PROJECTION	DATE	BY	DATE	BY	DATE
DO NOT SCALE DRAWING	DATE	BY	DATE	BY	DATE
REMOVE DIMES AND BREAK SHARP CORNERS	DATE	BY	DATE	BY	DATE
NATIONAL	DATE	BY	DATE	BY	DATE
FINISH	DATE	BY	DATE	BY	DATE
TITLE		FLOW DIAGRAM			
DRAWING NUMBER		MS11-P-3			